

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	7153	(365/145 365/189.11 365/202 365/203 365/207).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 18:09
L2	155	(first near3 current near3 (source sink)) same (second near3 current near3 (source sink)) same (bitline digitline (bit adj line) (digit adj line))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 18:10
L3	40	L1 and L2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 18:11
L4	19699	(ferroelectric and memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 18:11
L5	4	L3 and L4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 18:11

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	18661	(ferroelectric and memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 16:04
S2	1470	(sense adj2 (amp amplifier)) same (((current adj2 source) potential bias volt\$3) near3 (connect\$3 coupl\$3) near3 (bitline digitline (bit adj line) (digit adj line)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/20 20:23
S3	170	S2 and S1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/16 21:45
S4	1300	S2 not S3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/16 21:45
S5	170	S2 not S4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/16 21:47
S6	308	S1 and ((2T/2C) (two adj3 transistor adj3 two adj3 capacitor))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/16 21:48
S7	37	S2 and S6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/16 21:48
S8	1433	S2 not S7	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/16 21:48
S9	37	S2 not S8	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/16 21:48

S10	1169	((bitline digitline (bit adj line) (digit adj line)) with (current adj3 source))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/20 15:39
S11	111	((bitline digitline (bit adj line) (digit adj line)) with (current adj3 source)) with (second adj3 (current adj3 source))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/20 15:43
S12	18685	(ferroelectric and memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/20 15:43
S13	6	S11 and S12	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/20 15:43
S14	9807	(sense adj2 (amp amplifier)) same (((current adj2 source) potential bias volt\$3) near3 (bitline digitline (bit adj line) (digit adj line)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/20 20:26
S15	18685	(ferroelectric and memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/20 20:24
S16	18685	S15	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/20 20:24
S17	1126	S14 and S16	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/20 20:24
S18	870	S14 and (plate adj3 line)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/20 20:27
S19	617	S15 and S18	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/20 20:25

S20	6578	(sense adj2 (amp amplifier)) same (((current adj2 source) potential bias volt\$3) with (coupl\$3 connect\$3) with (bitline digitline (bit adj line) (digit adj line)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/20 20:26
S21	610	S20 and (plate adj3 line)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/20 20:27
S22	418	S21 and S15	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/20 21:12
S23	3	"L22" and (latch near3 (sense adj (amp amplifier)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/20 21:12
S24	18685	(ferroelectric and memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/20 21:12
S25	6578	(sense adj2 (amp amplifier)) same (((current adj2 source) potential bias volt\$3) with (coupl\$3 connect\$3) with (bitline digitline (bit adj line) (digit adj line)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/20 21:12
S26	610	S25 and (plate adj3 line)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/20 21:12
S27	418	S26 and S24	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/20 21:12
S28	65	S27 and (latch near3 (sense adj (amp amplifier)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/20 21:13
S29	18703	(ferroelectric and memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/21 13:14

S30	6585	(sense adj2 (amp amplifier)) same (((current adj2 source) potential bias volt\$3) with (coupl\$3 connect\$3) with (bitline digitline (bit adj line) (digit adj line)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/21 13:14
S31	610	S30 and (plate adj3 line)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/21 13:14
S32	418	S31 and S29	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/21 13:15
S33	90	S32 and ((precharg\$3 charg\$3 discharg\$3) adj3 ground)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/21 13:16
S34	6585	(sense adj2 (amp amplifier)) same (((current adj2 source) potential bias volt\$3) with (coupl\$3 connect\$3) with (bitline digitline (bit adj line) (digit adj line)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/21 13:16
S35	610	S34 and (plate adj3 line)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/21 13:16
S36	155	S35 and ((precharg\$3 charg\$3 discharg\$3) adj3 ground)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/21 13:18
S37	90	S36 and S29	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/21 13:17
S38	65	S36 not S37	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/21 13:17
S39	90	S36 not S38	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/21 13:17

S40	155	S35 and (((pre adj charg\$3) precharg\$3 charg\$3 discharg\$3) adj3 ground)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/21 13:20
S41	240	S35 and (((pre adj charg\$3) precharg\$3 charg\$3 discharg\$3) with ground)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/21 13:20
S42	153	S41 and S29	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/21 13:20
S43	87	S41 not S42	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/21 13:21
S44	153	S41 not S43	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/21 13:29
S45	24	S44 and (discharg\$3 adj3 ground)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/21 13:30
S46	76	S44 and ((charg\$3 precharg\$3 (pre adj charg\$3)) adj3 ground)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/21 13:55
S47	18703	(ferroelectric and memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/21 13:55
S48	6585	(sense adj2 (amp amplifier)) same (((current adj2 source) potential bias volt\$3) with (coupl\$3 connect\$3) with (bitline digitline (bit adj line) (digit adj line)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/21 13:55
S49	610	S48 and (plate adj3 line)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/21 13:55

S50	240	S49 and (((pre adj charg\$3) precharg\$3 charg\$3 discharg\$3) with ground)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/21 13:55
S51	153	S50 and S47	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/21 13:55
S52	87	S50 not S51	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/21 13:55
S53	153	S50 not S52	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/21 13:55
S54	90	S53 and ((charg\$3 precharg\$3 (pre adj charg\$3) discharg\$3) adj3 ground)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/21 14:33
S55	18703	(ferroelectric and memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/21 14:33
S56	6585	(sense adj2 (amp amplifier)) same (((current adj2 source) potential bias volt\$3) with (coupl\$3 connect\$3) with (bitline digitline (bit adj line) (digit adj line)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/21 14:33
S57	610	S56 and (plate adj3 line)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/21 14:33
S58	240	S57 and (((pre adj charg\$3) precharg\$3 charg\$3 discharg\$3) with ground)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/21 14:33
S59	153	S58 and S55	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/21 14:33

S60	87	S58 not S59	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/21 14:33
S61	153	S58 not S60	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/21 14:33
S62	19	S61 and ((charg\$3 precharg\$3 (pre adj charg\$3) discharg\$3) adj3 (vdd vcc))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/21 14:33
S63	19699	(ferroelectric and memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 15:19
S64	6835	(sense adj2 (amp amplifier)) same (((current adj2 source) potential bias volt\$3) with (coupl\$3 connect\$3) with (bitline digitline (bit adj line) (digit adj line)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 15:19
S65	659	S64 and (plate adj3 line)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 15:19
S66	266	S65 and (((pre adj charg\$3) precharg\$3 charg\$3 discharg\$3) with ground)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 15:19
S67	174	S66 and S63	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 15:19
S68	92	S66 not S67	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 15:19
S69	174	S66 not S68	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 15:19

S70	21	S69 and ((charg\$3 precharg\$3 (pre adj charg\$3) discharg\$3) adj3 (vdd vcc))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 15:19
S71	17	(current adj3 source) with (bitline digitline (bit adj line) (digit adj line)) with read\$3 with (before prior)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 15:58
S72	19699	(ferroelectric and memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 15:57
S73	0	S71 and S72	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 15:57
S74	0	S71 and ferroelectric	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 15:58
S75	84	(current adj3 source) with (bitline digitline (bit adj line) (digit adj line)) same read\$3 same (before prior)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 15:58
S76	17	S75 and ferroelectric	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 15:58
S77	67	S75 not S76	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 15:58
S78	17	S75 not S77	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 15:58

S79	1	(first near3 current near3 (source sink)) same (second near3 current near3 (source sink)) same (bitline digitline (bit adj line) (digit adj line)) same ((complement\$3 negative) adj3 (bitline digitline (bit adj line) (digit adj line))) same read\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 16:08
S80	19699	(ferroelectric and memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 16:07
S81	155	(first near3 current near3 (source sink)) same (second near3 current near3 (source sink)) same (bitline digitline (bit adj line) (digit adj line))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 16:08
S82	6	S81 and S80	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 16:08
S83	149	S81 not S82	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 16:08
S84	6	S81 not S83	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 16:08